
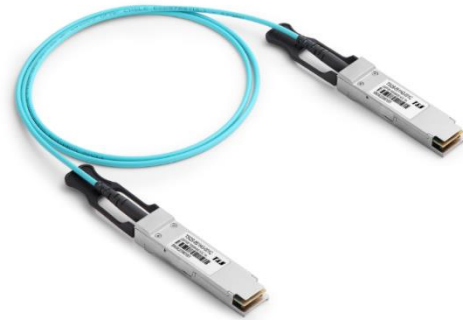


100G QSFP28 Active Optical Cable TSQS-851HG-xxxC

Features

- Four-channel full-duplex active optical cable with QSFP28 plugs
- Compliant to the IEEE 802.3bm(100GBASE-SR4)
- Compliant to the SFF-8665 Specification
- VCSEL Array Transmitter and PIN Array Receiver
- Low Power Dissipation <2.5W per end
- Operating case temperature 0°C to 70°C
- RoHS compliant 



Applications

- IEEE 802.3bm 100GBASE SR4 Ethernet

Product Description

The TSQS-851HG-XXXC is a Four-Channel, Pluggable, Parallel, Fiber-Optic QSFP28 AOC for 100 Gigabit Ethernet, Infiniband FDR/EDR Applications. This transceiver is a high performance module for short-range multi-lane data communication and interconnect applications. It integrates four data lanes in each direction with 100Gbps bandwidth.

This module is designed to operate over multimode fiber systems using a nominal wavelength of 850nm. The electrical interface uses a 38 contact edge type connector.

Absolute Maximum Rating

Parameters	Symbol	Min.	Max.	Unit
Power Supply Voltage	VCC	-0.5	+3.6	V
Storage Temperature	Tc	-40	+85	°C
Relative Humidity	RH	0	85	%

Recommended Operating Environment

Parameter	Symbol	Min	Typical	Max	Unit
Supply Voltage	VCC	3.15	3.30	3.45	V
Supply current per end	Icc	-	-	725	mA
Case Operating Temperature	Tca	0	-	70	°C

Information and specifications are subject to change without notice.
Please visit www.china-tscom.com for more information

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Data Rate Per Lane	DR	-	25.78125	-	Gbps
Fiber Bend Radius	Rb	3	-	-	cm

Notes:

- [1] Supply current is shared between VCCTX and VCCR_X.
- [2] In-rush is defined as current level above steady state current requirements.

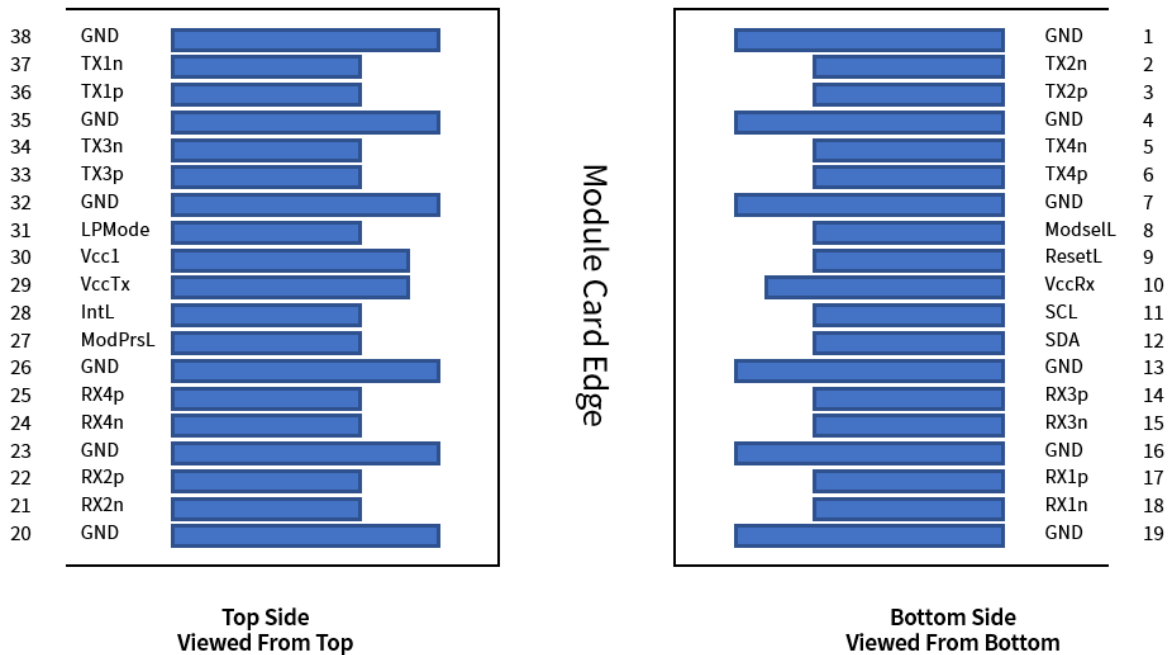
Transmitter Specifications

Parameter	Symbol	Min	Typical	Max	Unit
Input differential impedance	R _{in}	90	100	110	Ω
Differential Input Voltage swing, per lane	V _{in}	300	-	1100	mV
Transmit Disable Voltage	VD	2.0	-	VCC+0.3	V
Transmit Enable Voltage	V _{en}	V _{ee}	-	V _{ee} +0.8	V

Receiver Specifications

Parameter	Symbol	Min	Typical	Max	Unit
Differential Output Swing, per lane	V _{out}	500	-	800	mV
Bit Error Rate	BER	-	-	10 ⁻¹²	-
Output Differential Impedance	R _{out}	90	100	110	Ω
Loss of Signal –Asserted	-	2.0	-	VCC+0.3	V
Loss of Signal –Negated	-	V _{ee}	-	V _{ee} +0.8	V

QSFP28 Transceiver Electrical Pad Layout



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Pin Definition

Pin	Symbol	Name/Description
1	GND	Ground
2	Tx2n	Transmitter Inverted Data Input
3	Tx2p	Transmitter Non-Inverted Data Input
4	GND	Ground
5	Tx4n	Transmitter Inverted Data Input
6	Tx4p	Transmitter Non-Inverted Data Input
7	GND	Ground
8	ModSelL	Module Select
9	ResetL	Module Reset
10	Vcc Rx	+3.3 V Power supply receiver
11	SCL	2-wire serial interface clock
12	SDA	2-wire serial interface data
13	GND	Ground
14	Rx3p	Receiver Non-Inverted Data Output
15	Rx3n	Receiver Inverted Data Output
16	GND	Ground
17	Rx1p	Receiver Non-Inverted Data Output
18	Rx1n	Receiver Inverted Data Output
19	GND	Ground
20	GND	Ground
21	Rx2n	Receiver Inverted Data Output
22	Rx2p	Receiver Non-Inverted Data Output
23	GND	Ground
24	Rx4n	Receiver Inverted Data Output
25	Rx4p	Receiver Non-Inverted Data Output
26	GND	Ground
27	ModPrsL	Module Present
28	IntL	Interrupt
29	VCC Tx	+3.3 V Power supply transmitter
30	VCC1	+3.3 V Power Supply
31	LPMODE	Low Power Mode
32	GND	Ground
33	Tx3p	Transmitter Non-Inverted Data Input
34	Tx3n	Transmitter Inverted Data Input
35	GND	Ground
36	Tx1p	Transmitter Non-Inverted Data Input
37	Tx1n	Transmitter Inverted Data Input
38	GND	Ground

Ordering Information

Part Number	Product Description
TSQS-851HG-XXXC	QSFP28 100G AOC 0°C ~ +70°C
XXX :001~070,1~70 Length in meters on OM3 MMF	
XXX :001~100,1~100 Length in meters on OM4 MMF	

References

1. SFF-8636 Specification for QSFP28.

Important Notice

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